

L Number	Hits	Search Text	DB	Time stamp
37	152	439.clas. AND ((IC ADJ card) SAME cover)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 18:40
38	22668346	(@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 18:41
39	133	(439.clas. AND ((IC ADJ card) SAME cover)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 18:42
40	119	(439.clas. AND ((IC ADJ card) SAME cover)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808)) AND (backup OR (dual OR two OR second))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 18:55
41	2	(439.clas. AND ((IC ADJ card) SAME cover)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808)) AND (backup)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 19:21
-	18024	smart ADJ card	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 09:50
-	7003	smart ADJ card.AB.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 09:50
-	23639	(dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 09:52
-	17	((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 (smart ADJ card)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 09:52
-	3	((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 (smart ADJ card.AB.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 09:53
-	2	((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 (smart ADJ card.AB.) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:06
-	12	((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 (smart ADJ card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:18
-	514	713/189.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 13:03

-	27	((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 ((smart ADJ card) OR (IC ADJ card) OR (Integrated ADJ Circuit ADJ Card))) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 18:36
-	11	((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 ((smart ADJ card) OR (IC ADJ card) OR (Integrated ADJ Circuit ADJ Card)).AB.) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:05
-	2	235/375-381.ccls. AND (backup ADJ processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:09
-	206	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) WITH (backup)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:14
-	155	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) WITH (backup)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:10
-	2	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) WITH (backup ADJ (processor OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:16
-	2	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) WITH (backup ADJ (process\$2 OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:17
-	2	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (backup ADJ (process\$2 OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:20
-	3	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (backup ADJ (process\$3 OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:17
-	3	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME ((backup OR redundant) ADJ (process\$2 OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:20
-	9	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (backup NEAR (process\$2 OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:22
-	29	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (backup NEAR5 (processor OR processing OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:58
-	32	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (backup NEAR5 (processor OR processing OR (control ADJ unit) OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:58

-	3	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (backup NEAR5 (processor OR processing OR (control ADJ unit) OR circuit))) NOT (((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (backup NEAR5 (processor OR processing OR circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:59
-	111	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME (remove\$4 NEAR5 (processor OR processing OR circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 13:54
-	317	714/13.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 13:18
-	1	714/13.ccls. AND ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 13:24
-	9418	235/375-381.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 13:25
-	2628	235/375-381.ccls. AND ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 13:26
-	18	((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) SAME ((remove\$4 NEAR5 cover) AND (remove\$4 SAME (processor OR processing OR circuit)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/26 18:39
-	60	713/192.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 15:18
-	703	713/193.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 15:18
-	106	380/227.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 15:20
-	32	380/229.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 15:20
-	15	(US-6501962-\$ or US-5786988-\$ or US-4926479-\$ or US-5586270-\$ or US-5097115-\$ or US-6575835-\$ or US-5497297-\$).did. or (US-20020105232-\$).did. or (EP-363122-\$).did. or (JP-07073113-\$).did. or (DE-29819389-\$ or DE-19526672-\$ or EP-363122-\$ or FR-2718549-\$ or JP-10055416-\$).did.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/20 15:47

-	2	((US-6501962-\$ or US-5786988-\$ or US-4926479-\$ or US-5586270-\$ or US-5097115-\$ or US-6575835-\$ or US-5497297-\$).did. or (US-20020105232-\$).did. or (EP-363122-\$).did. or (JP-07073113-\$).did. or (DE-29819389-\$ or DE-19526672-\$ or EP-363122-\$ or FR-2718549-\$ or JP-10055416-\$).did.) AND \$2crypt\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/04/20 15:47
-	28	((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:19
-	17	signal AND (((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:19
-	1	(activ\$5 NEAR2 signal) AND (((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:24
-	0	(differ\$3 NEAR2 signal) AND (((dual OR two OR twin OR backup OR second\$3) ADJ (processor OR chip)) NEAR10 ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:25
-	790	(activat\$3 NEAR2 signal) AND ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:26
-	1	(differ\$3 NEAR2 activat\$3 NEAR2 signal) AND ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:27
-	559	(activat\$3 NEAR2 signal) AND ((IC OR (Integrated ADJ Circuit) OR smart) ADJ (card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:29
-	361	(activat\$3 NEAR2 signal) AND ((smart) ADJ (card)) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 08:29
-	17	((activat\$3 NEAR2 signal) WITH ((smart) ADJ (card))) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 09:43
-	1	(processor WITH (decrypt\$3 NEAR3 signal) WITH ((smart) ADJ (card))) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 09:44
-	29	((decrypt\$3 NEAR3 signal) WITH ((smart) ADJ (card))) AND ((@ad<=20000808) OR (@prad<=20000808) OR (@ptad<=20000808) OR (@frpd<=20000808))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/21 09:45